## MAE140 - Linear Circuits - Winter 17 <br> Midterm, February 9

## Instructions

(i) This exam is open book. You may use whatever written materials you choose, including your class notes and textbook. You may use a hand calculator with no communication capabilities
(ii) You have 75 minutes
(iii) Do not forget to write your name and student number

Good luck!


Figure 1: Circuits for all questions.

## 1. Equivalent circuits

Part I: [2 points] Turn off all the sources in the circuit of Figure 1(a) and find the equivalent resistance as seen from terminals (A) and (B).

Solution: Part I: We start by switching off the sources.


Part II: [4 points] Find the voltage $v_{0}$ using only superposition, source transformations, voltage division, and current division.

Solution: Part II: To use superposition, we first turn off the independent current source

We substitute the current source by an open circuit. Then, we get the circuit on the right
(+ . 5 point)

The 200hms resistors does not have any current going through it, so we can just redraw the circuit as follows
(+ . 5 point)


Now, using voltage division, we obtain

$$
v_{o, 1}=\frac{12}{12+60}(-12)=-2 V
$$

Next, we turn off the independent voltage source.

We substitute the voltage source by a closed circuit. Then, we get the circuit on the right
(+ . 5 point)


The current source is in series with the 200 Ohms resistor. From the point of view of the rest of the circuit, there is no difference whether the resistor is there or not. Therefore, we use source transformation to get the circuit on the right

## (+ . 5 point)



Using now current division and Ohm's law, we obtain

$$
\begin{equation*}
v_{0,2}=12 \frac{1 / 12}{1 / 12+1 / 60}(1)=10 \mathrm{~V} \tag{+.5point}
\end{equation*}
$$

By superposition, we conclude that

$$
v_{0}=v_{0,1}+v_{0,2}=-2+10=8 \mathrm{~V}
$$

Part III: [1 point] What is the Thévenin equivalent of the circuit as seen from terminals (A) and (B)?

Solution: Part III: We have computed the equivalent resistance from terminals (A) and (B) with all sources turned off in Part I, and the open-circuit voltage in Part II. Therefore, the Thévenin equivalent of the circuit is simply

(+ 1 point)
Part IV: [ 1 point] Find the power absorbed by a $30 \Omega$ resistor that is connected to terminals (A) and (B).

## Solution:

Part IV: We use the Thévenin equivalent of the circuit to obtain the answer in an easy way. Connecting the 30 Ohms resistor gives rise to the circuit


$$
\text { (+ . } 5 \text { point })
$$

By voltage division, the voltage drop across the load is

$$
v=\frac{30}{30+10}(8)=6 V
$$

Therefore, the power absorbed by the resistor load is

$$
P=v^{2} G=(6)^{2} \frac{1}{30}=1.2 \mathrm{~W}
$$

$$
(+.5 \text { point })
$$

## 2. Node voltage analysis

Part I: [5 points] Formulate node-voltage equations for the circuit in Figure 1(b). Use the node labels (A) through (E) provided in the figure and clearly indicate how you handle the presence of a voltage source. The final equations must depend only on unknown node voltages and the resistor values $R_{1}$ through $R_{5}$. Do not modify the circuit or the labels. No need to solve any equations!

Solution: Part I: There are five nodes in this circuit. The ground node (E) (hence $v_{E}=0$ ) has already been chosen for us. Unfortunately, with this choice, the ground node is not directly connected to the voltage source, so we cannot use method 2 to take care of it. The voltage source is also not in series with any resistor, so method 1 is discarded too. Therefore, we are left with method 3, a supernode (A)+(D)).
(+ 1 point)

The equation for the supernode is

$$
\begin{equation*}
v_{A}-v_{D}=v_{S} \tag{+1point}
\end{equation*}
$$

KCL at the supernode takes the form

$$
G_{2}\left(v_{A}-v_{B}\right)+G_{3}\left(v_{D}-v_{B}\right)+i_{S}=0
$$

(+ 1 point)
where, as we do usually, we denote $G_{i}=1 / R_{i}$.
We also need to write KCL at nodes (B) and (C). These take the form

$$
\begin{array}{ll}
G_{2}\left(v_{B}-v_{A}\right)+G_{3}\left(v_{B}-v_{D}\right)+G_{4}\left(v_{B}-v_{C}\right)=0 & (+1 \text { point }) \\
G_{4}\left(v_{C}-v_{B}\right)+G_{5} v_{C}=0 & (+1 \text { point })
\end{array}
$$

This gives us a total of 4 equations in the 4 node voltage unknowns $v_{A}, v_{B}, v_{C}, v_{D}$. In matrix form, we can write this as

$$
\left(\begin{array}{cccc}
1 & 0 & 0 & -1 \\
G_{2} & -G_{2}-G_{3} & 0 & G_{3} \\
-G_{2} & G_{2}+G_{3}+G_{4} & -G_{4} & -G_{3} \\
0 & -G_{4} & G_{4}+G_{5} & 0
\end{array}\right)\left(\begin{array}{c}
v_{A} \\
v_{B} \\
v_{C} \\
v_{D}
\end{array}\right)=\left(\begin{array}{c}
v_{S} \\
-i_{S} \\
0 \\
0
\end{array}\right)
$$

Part II: [1 point] Provide expressions for the voltage $v_{x}$ and the current $i_{x}$ in terms of node voltages.
Solution: Part II: In terms of the node voltages, $v_{x}$ and $i_{x}$ can be expressed as

$$
\begin{aligned}
v_{x} & =v_{A}-v_{B} \\
i_{x} & =G_{3}\left(v_{B}-v_{D}\right)
\end{aligned}
$$

Part III: [1 bonus point] If it was up to us, would you have chosen ground differently? Justify your answer.
Solution: Yes! Either node (A) or (D) are better choices, because they are directly connected to the voltage source, and any of those choices would allow us to use method 2 (instead of having to resort to method 3 - supernode).
(+ 1 bonus point)

## 3. Mesh current analysis

Part I: [5 points] Formulate mesh-current equations for the circuit in Figure 1(b). Use the mesh currents shown in the figure and clearly indicate how you handle the presence of the current source. The final equations should only depend on the unknown mesh currents and the resistor values $R_{1}$ through $R_{5}$. Do not modify the circuit or the labels. No need to solve any equations!

Solution: Part I: There are three meshes in this circuit. The current source, $i_{S}$, belongs to only one mesh, so we can use method 2 to deal with it.
(+ 1 point)
Consequently, we set

$$
i_{3}=-i_{S}
$$

We write KVL for each of the other 2 meshes. For mesh 1, KVL reads like

$$
\begin{equation*}
R_{1}\left(i_{1}-i_{2}\right)-v_{S}=0 \tag{+1.5point}
\end{equation*}
$$

For mesh 2, KVL reads like

$$
\begin{equation*}
R_{1}\left(i_{2}-i_{1}\right)+R_{2} i_{2}+R_{3}\left(i_{2}-i_{3}\right)=0 \tag{+1.5point}
\end{equation*}
$$

This gives us a total of 3 equations in the 3 mesh current unknowns $i_{1}, i_{2}, i_{3}$. In matrix form, we can write this as

$$
\left(\begin{array}{ccc}
0 & 0 & 1 \\
R_{1} & -R_{1} & 0 \\
-R_{1} & R_{1}+R_{2}+R_{3} & -R_{3}
\end{array}\right)\left(\begin{array}{c}
i_{1} \\
i_{2} \\
i_{3}
\end{array}\right)=\left(\begin{array}{c}
-i_{S} \\
v_{S} \\
0
\end{array}\right)
$$

Alternatively, one may substitute the value of $i_{3}$ into the KVL equation for mesh 2 , and set up a system of 2 equations with 2 unknowns $i_{1}$ and $i_{2}$.

Part II: [1 point] Provide expressions for the voltage $v_{x}$ and the current $i_{x}$ in terms of mesh currents.
Solution: Part II: In terms of the node voltages, $v_{x}$ and $i_{x}$ can be expressed as

$$
\begin{aligned}
v_{x}=R_{2} i_{2} & (+.5 \text { point }) \\
i_{x}=i_{2}-i_{3} & (+.5 \text { point })
\end{aligned}
$$

Part III: [1 bonus point] Would changing the value of the resistors $R_{4}$ and $R_{5}$ have any effect on the mesh currents? Is this consistent with what we know about source transformations?

Solution: The value of these resistors does not have any effect on the mesh currents. This is because, from what we have learned about source transformations, the portion of the circuit composed by $R_{4}, R_{5}$, and $i_{S}$ is equivalent, from the point of view of the rest of the circuit, to just the current source $i_{S}$. This can also be seen in the mesh current equations we wrote in Part I, where $R_{4}$ and $R_{5}$ do not appear. Changing the value of those resistors would definitely change the voltage drop seen by the current source.
(+ 1 bonus point)

