1._ Part I

To use mode-voltage analysis, we must take care of the presence of the voltage source very one of the three hrethods discussed in days:

- 1) source tomsformation
- 2) groondrig à mode conveniently

3) apernode We cannot use 1) because the voltage pource is not in series with a resistor (even if it was, the striement of the gration explicitly roles out undifying the arrit, which also discards surre toursformation). 2) is instead applicable, because the ground has been alread in a convenient way. One could also use 3) (because it is always applicable) but that would overapplicate things



So we settle on vong method 2, which gives $V_{A} = V_{S}$ [+1 point]

And we write KCL egs for modes B, C, & D. We do it by inspection, which is faster.

H4 points (Here, we have used the short-hand notation $G_i = \frac{1}{R_i}$) This gives 4 egs in 4 unknowne, uniter in natore form. If you erkstate Vy=Vs in the other 3 eps, there you can also exposes this al 3 egs in 3 onknowns.



The terms of the mode voltages, we have $V_x = V_C$ [+1 point] $i_x = G_4(V_A - V_C)$ [+1 point]

Part II

The resistor R_4 is in series with the arrest source. From what we know from class, a current source in series with a resistor is equivalent, from the point of view of the rest of the armit, to put hours the arrest source. This is depicted in the diggen $K_1 i$ $i_s + v_{R^-} + i_s$ $i_s + v_{R^-}$ Rest of cet $i = i_S$ $i_s + v_{R^-}$ Rest of cet

This means that, if the technizian replaced the resistor R4 M our avait by a short avait, then nothing changed in the rest of the avait, so the values of Vx and ix remained the same. [+1 point] 2._ Part I

We tron off all the sources in the crait and down the armit below





[to.5 point]



= 32

Part II We tom off the current source, substituting it by an open cirant as



Since there is no correct flowing through the resistor Ry, we redraw this as







Since the resistur Ry is in sories with the arrest Forree, we can simply redraw the epivolent arait



We use the fact that Ry and Ry are m prallel to redraw the avant as



[+0.5 pout]

Next, we combre the resistors in series,



RIR2 + (K1+K2)(K3+K5) [+1 point] Part IV By superposition, the open circuit wilkge as seen from terminals (B) and (B) is the som of the answers of Part II (everent source off) and Part III (voltage source off). [+0.5 point]

Therefore, $V_T = V_{OC} = (V_{AB})_1 + (V_{AB})_2 =$ $= \frac{R_2 R_5}{(R_3 + R_5)(R_1 + R_2) + R_1 R_2} \cdot V_S - \frac{(R_1 R_2 + R_3 (R_1 + R_2))R_5}{R_1 R_2 + (R_1 + R_2)(R_3 + R_5)} i_S$ $= R_5 \cdot \frac{R_2 V_S - (R_1 R_2 + R_3 (R_1 + R_2)) i_S}{R_1 R_2 + (R_1 + R_2) (R_3 + R_5)} = 5 \sqrt{\frac{R_2 V_S - (R_1 R_2 + R_3 R_3 + R_5)}{R_1 R_2 + (R_1 + R_2) (R_3 + R_5)}}$ From Part I, $R_{7} = R_{EQ} = \frac{R_{5} \cdot (R_{1}R_{2} + R_{3}(R_{1} + R_{2}))}{(R_{1} + R_{2})(R_{3} + R_{5}) + R_{1}R_{2}} = \frac{31}{31}$ Therefre, the Therean equivalent is

 $V_7 \left(\begin{array}{c} + \\ - \end{array} \right)$

[+0.5 pout]

Part V

$$= 5 \frac{5 \cdot 10 - (25 + 50)(-1)}{25 + 100} = 5V$$

$$\mathcal{R}_{7} = \frac{\mathcal{R}_{5} \cdot (\mathcal{R}_{1} \mathcal{R}_{2} + \mathcal{R}_{3} (\mathcal{R}_{1} + \mathcal{R}_{2}))}{(\mathcal{R}_{1} + \mathcal{R}_{2})(\mathcal{R}_{3} + \mathcal{R}_{5}) + \mathcal{R}_{1} \mathcal{R}_{2}} =$$

$$= \frac{5(25+5(10))}{100+25} = 3.2$$



 $V = \frac{12}{A_{Z+3}} \cdot 5 = 0.8.5 = 4V$

[+1 extra point]

The power provided to the resistor is then $P = \frac{1}{R} v^2 = \frac{1}{12} \cdot 4^2 = 1.38 W$ This means that the minimum power why of the 42.2- resistor has to be 1.33W. [+ 1 extra point]

3 The why is that we goed plant of
the real-world power supply (rwps) is:
provide 24V up to 1A current So, for
R = 100.2, we have
$i = \frac{24}{100} = 0.244 < 14$, which within [50.5 points] Spece.
For R=10, we have
$i = \frac{24}{10} = 2.44 > 14$, out of spec [+0.5 points]
Fir R=1, we have
$i = \frac{24}{1} = 241 > 1A$, out of spece pointer
So the task of output voltage from the rups
looks like [+05 wints]
R volkge orfgat
100L 24V
122 less than 24V, OV, or broken
12 los flour 24V, OV, or brokent