1._ Part I

To use mode-voltage analysis, we must take care of the presence of the voltage source very one of the three hrethods discussed in days:

- 1) source tomsprimation
- 2) groondrig à mode conveniently
- 3) aperusde

We cannot use 1) because the voltage pource is not in series with a resistor (even if it was, the striement of the greation explicitly roles out undifying the arrit, which also discards surre toursformation). 2) cannot be used either, because the ground node (which has already love chosen) is not placed conveniently. So we are left with method 3), where we combrue modes A and (B) to create a systude.

[+2 prints]



The equation for the superiode is $V_{A} - V_{B} = V_{S}$ [+1 point] Next, we write KCL for the supernide, $G_1(V_A) + G_3V_B + G_4(V_A - V_C) = I_S[+1 print]$ (Here, we have vad the shorthand notition $G_i = \frac{1}{R_i}$. Next, we write KCL for mode @, $G_{4}(v_{c}-v_{A})+G_{5}v_{c}+i_{s}=0 \quad [+1 \text{ prive}]$ In matrix form, we have $\begin{pmatrix} 1 & -1 & 0 \\ G_{1}+G_{4} & G_{3} & -G_{4} \\ -G_{4} & 0 & G_{4}+G_{5} \\ \end{pmatrix} \begin{pmatrix} V_{A} \\ V_{B} \\ V_{C} \end{pmatrix} = \begin{pmatrix} V_{S} \\ i_{S} \\ -i_{S} \\ -i_{S} \\ \end{pmatrix} \begin{bmatrix} +1 & \text{point} \\ -i_{S} \\ \end{bmatrix}$ This gives 3 equipons in 3 onknowns.



Juterus of the mode voltages, we have $V_x = V_B - V_C$ [+1 point] $i_x = G_4 (V_A - V_C)$ [+1 point]

Part II

Changing the value of R2 world not affect the value of Vx and ix. This can be justified in various ways. For instance, looking at the equations we wrote in Part I, R2 area not appear in any. That means that its value does not affect the value of the mode voltages, have it does not affect the value of Vx and ix. Another way to justify it is to realize that R2 is in parallel with the voltage source.





We from off the source in the avail and total



where the current source gets replaced Sy an open circuit. [+1 point. L+1 point Next, we use association of resistors to find the equivalent resistance. The two R-resistors on the left are the Series, so we andre them 2R \$ \$2R R [+1point] The resulting resistur is in parallel with the vertical 2R-veristor, which is also in







We are hold to compute the open-avait voltage as seen from terminales (A) (B). Since there is no correct flowing through the H/2 resistor, we can simply consider the following



So the voltage drop we are to compute is the one seen by the R-resistor on the right (which is the same as the one seen by the 2R-resistor, since they are in parallel). We consome the resistors in parallel to Stam $2R/R = \frac{2R^2}{3R} = \frac{2R}{3}$





We already have all the information he need from our answers to Ports Id II. The Théveur apprendent avait is _0(K) -1110-R = R $= \frac{Ris}{4}$ [+1 point] The Norton equivalent avait is = RN=+ [+1 point]



Connecting a fise and a resistor in Series results in



