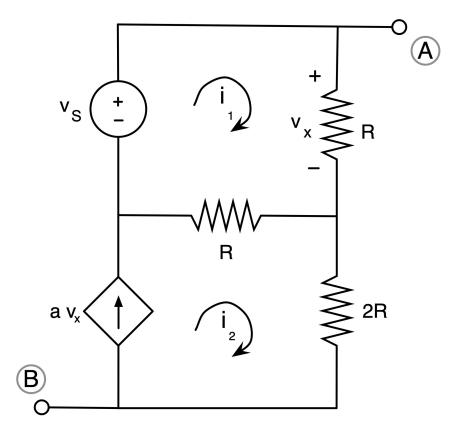
## 1.\_ Part I



Looking at the avanit, we observe the presence of 1 corrent source, which is a problem we need to deal with to use mesh arrest analysis. The ament source belonge to one mesh, so we just use  $i_2 = aV_x$  [method 2] [+1 point] KVL for the top mesh reads  $Ri_1 + R(i_1 - i_2) - V_s = 0$  [41 point] We also used to account for the presence of the dependent source, hooking at the arant, we see that [+1 port]  $V_x = Ri_1$ 

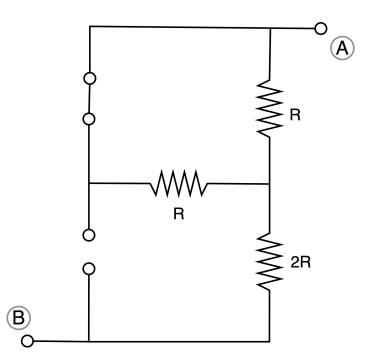
This discussion beads to a total of 3 eps n 3on knowns  $i_1, i_2, V_X$ . We now solve them to find the culluowns.

$$i_{2} = aV_{x} = aRi_{1}$$
Then
$$2Ri_{2} - aRi_{1}^{2} = V_{S} = D \quad R(2 - aR)i_{1}^{2} = V_{S}$$

$$i_{1} = \frac{V_{S}}{R(2 - aR)}$$
Therefore, the open errait voltage as seen
from terminals (P) and (B) is
$$V_{0C} = V_{AB} = Ri_{1} + 2Ri_{2} = Ri_{1} + 2Rai_{1}^{2} =$$

$$= \frac{R(1 + 2aR)}{R(2 - aR)}V_{S} \qquad [+1 \text{ point}]$$

Part I When we two off the independent some, the dependent one also zets tomed off. This can be seen in the expression for the mesh [+0.5 point] currents in Part I: if  $V_s = 0$ , then both  $i_1 = 0 = i_2$ , and hence  $V_x = 0$ . With the Sources off, the circuit holds like

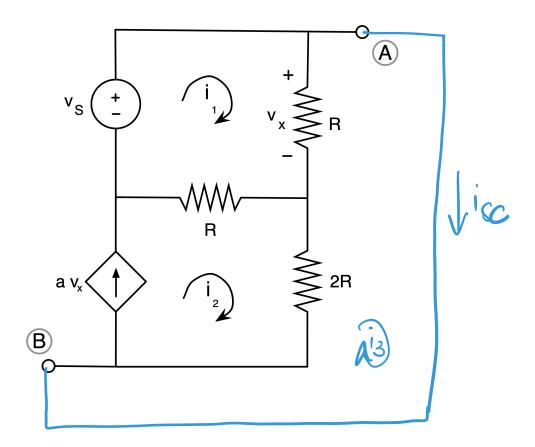




Note that the two R resistors are in parallel, so we draw  $R_{2}$   $R_{2}$  $R_{2}$ 



We connect terminale ( ) and ( ) and find the short-avail errent as



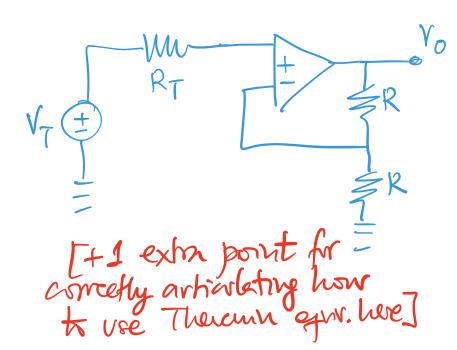
The dust crait arrest is  $i_3 = i_5c$ . We use mode analysis to find  $i_1, i_2, i_3$  (and  $v_x$ ). We deal with the correct source by writing  $i_2 = av_x$  (method 2) [40.5 point] We write KVL for medies 1 & 3. KVL for mesh 1 :  $R(i_1 - i_3) + R(i_1 - i_2) - V_5 = O$ [+0.5 point]

 $KVL \text{ for much } 3: 2R(i_3 - i_2) + R(i_3 - i_1) = 0$ Ve accort for the presence of the dependent source with  $V_x = R(i_1 - i_3)$   $V_x = keys in 4 onknowner (i_1, i_2, i_3, V_x). Starry,$ [+0.5 port] 3i3-2i2-i1=0 (from 3rd og)  $i_1 = 3i_3 - 2i_2$ Substituting in Zueley,  $2R(i_3 - i_2) + 3R(i_3 - i_2) = V_S = P 5R(i_3 - i_2) = V_S$ And  $i_2 = aV_x = aR(i_1 - i_3) = 2aR(i_2 - i_2)$ So  $i_2 = \frac{a_1 R V_S}{SR} = \frac{2a}{5} V_S$ Therefore,  $i_{sc} = i_3 = i_2 + \frac{V_s}{5R} = \frac{1+2aR}{5R} \frac{V_s}{5R}$ [+1 point] Part IV With the answers to Parts IdI, we have [+0.25 poruf]  $V_T = V_{AB} = \frac{1+2aR}{2-aR} V_S$  $R_{7} = R_{N} = \frac{V_{7}}{i_{sc}} = \frac{1+2\alpha R}{2-\alpha R} V_{s} \cdot \frac{5R}{1+2\alpha R} \cdot \frac{1}{K} = \frac{5R}{2-\alpha R} [+0.5 point]$ 

 $i_N = i_{SC} = \frac{1+2\alpha R}{5R} V_S$  [+0.25 point] The reason why  $R_T = R_N$  is not equal to  $R_{EQ}$ computed in Part II is because, when we ton off the IVS, the dependent source also gets toned off and its effect is not taken into account.

## Part V

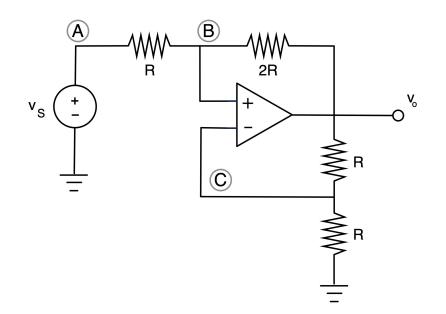
Computing this directly noved be grite mobled. However, we have already done all the hard work computing the Therewin equipilent. Connecting the circuits is the Same al



This is a woninverting op-amp, and therefore

 $V_0 = \frac{R + R}{R} V_7 =$  $=2Y_{T}=2\frac{1+2ak}{2-ak}V_{S}$ 

[+1 extra point]



Part I As instructed, we use usdal analysis to figure out the output voltage. [+1 point] We know  $V_A = V_S$ . KCL at node B gives us (with  $i_p = 0$ )  $\frac{1}{R}\left(V_{B}-V_{S}\right)+\frac{1}{2R}\left(V_{B}-V_{O}\right)=0$ [+1 point] KCL at usde C (with  $i_n = 0$ ),  $\frac{1}{R}\left(V_{C}-V_{O}\right)+\frac{1}{2}\left(V_{C}\right)=0$ [+1 point] From ideal conditions, we have [+1 point]  $V_{\mathcal{B}} = V_{\mathcal{C}}$ 

We have 3 eqs. in 3 continours 
$$V_B, V_C, V_0$$
,  
so we can solve. From the 2nd equation,  
 $V_0 = 2V_C = 2V_B$   
Substituting into the 1st quation,  
 $\frac{1}{R}(V_B-V_S) + \frac{1}{2R}(V_B - 2V_B) = 0 = P V_B = 2V_S$ 

Therefore  

$$V_0 = 2V_B = 4V_S$$
 [+1 point]

•

Part II When the engineer connects the lod resistor R\_= 10-2, the voltage drop this resistor sees is Vo. If the connected source is  $V_s = 3V_s$ , then  $4 \cdot V_S = 4 \cdot 3 = 12 > 10V$ , so the format ] op-amp gets saturated and  $V_0 = 10V$ . Thorepre, the power delivered to the had is  $P_L = V_0^2 \cdot \frac{1}{R_1} = \frac{100}{10} = 10W$  [+1 point]

The vange of values for the voltage source so that the op-amp operate Arready is -10V < Vo = 4Vs < 10V -2.5V < Vs < 2.5V [+1 point]

Since we want to design a crait with

V<sub>o</sub>

[+1 porut]

With Huis, we Love

 $V_0 = \frac{3R+R}{R}V_S =$ 

 $=4V_{S}$ 

Vo=4Vs, we use a non-inverting op-amp.

=R

ZR I I

VS (+) == == R == R

[+1 point for correct design]

Part III